

## PIC C Compiler 64 Bit

Parameters:



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For those of you using the PIC C Compiler for ARM, you can now choose between two compilers: 12 bit, or 64 bit. 64 bit not only doubles the size of the memory that the compiler can work with, but also allows it to use more registers and therefore more efficient code. The compiler can work with up to 14 registers. 12 bit compilers can use 12 registers, 64 bit compilers can use 16 registers. If you are using the PIC C Compiler for 12 bit, you can easily choose to stick to 12 bit all the time and use the 12 bit compiler. But if you are using the 64 bit compiler, you will have to use a 64 bit build. Programming Language Interfaces If your C program uses any language interfaces, you may find that the 64 bit compiler offers better performance. For example: void \*malloc (unsigned long sz); If you were previously compiling under the 12 bit compiler, you will likely find malloc performance noticeably reduced since 12 bit numbers can't represent values above 2147483648. This is because the language interfaces only support calling functions in 8 bit range and 16 bit range which have to have some padding to represent values outside of that range. This can be easily seen when printing an octet: printOctet (void \*p); Example #include void \*malloc (unsigned long sz); void \*malloc\_12bit (unsigned long sz); void \*malloc\_64bit (unsigned long sz); void printOctet(void \*p); void test(void) { printf ("%p ", malloc (1024)); printOctet(malloc(1024)); printOctet(malloc(1024\*2)); printOctet(malloc(1024\*3)); } void \*malloc(unsigned long sz) { return malloc (sz); } void \*malloc\_12bit(unsigned long sz) { return malloc (sz) + (sz-1); } void \*malloc\_64bit(unsigned long sz) { return malloc (sz) + (sz-1) + (sz-1) + (sz-1) - (sz-2) 3e33713323

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